

FIG. 1

Comparator to generate NORM signal

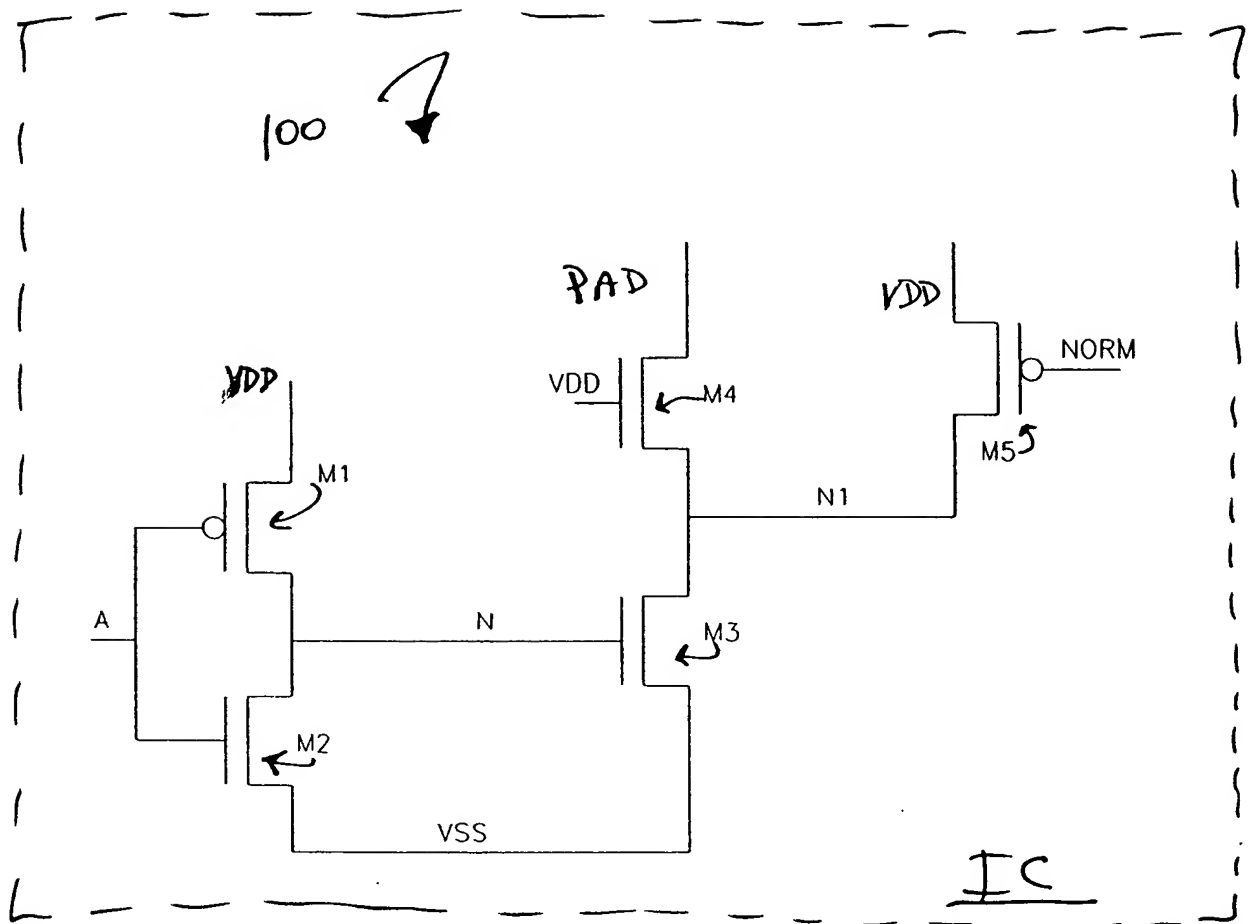


FIG. 2

Improved 5V Tolerant  
Open Drain Output Buffer

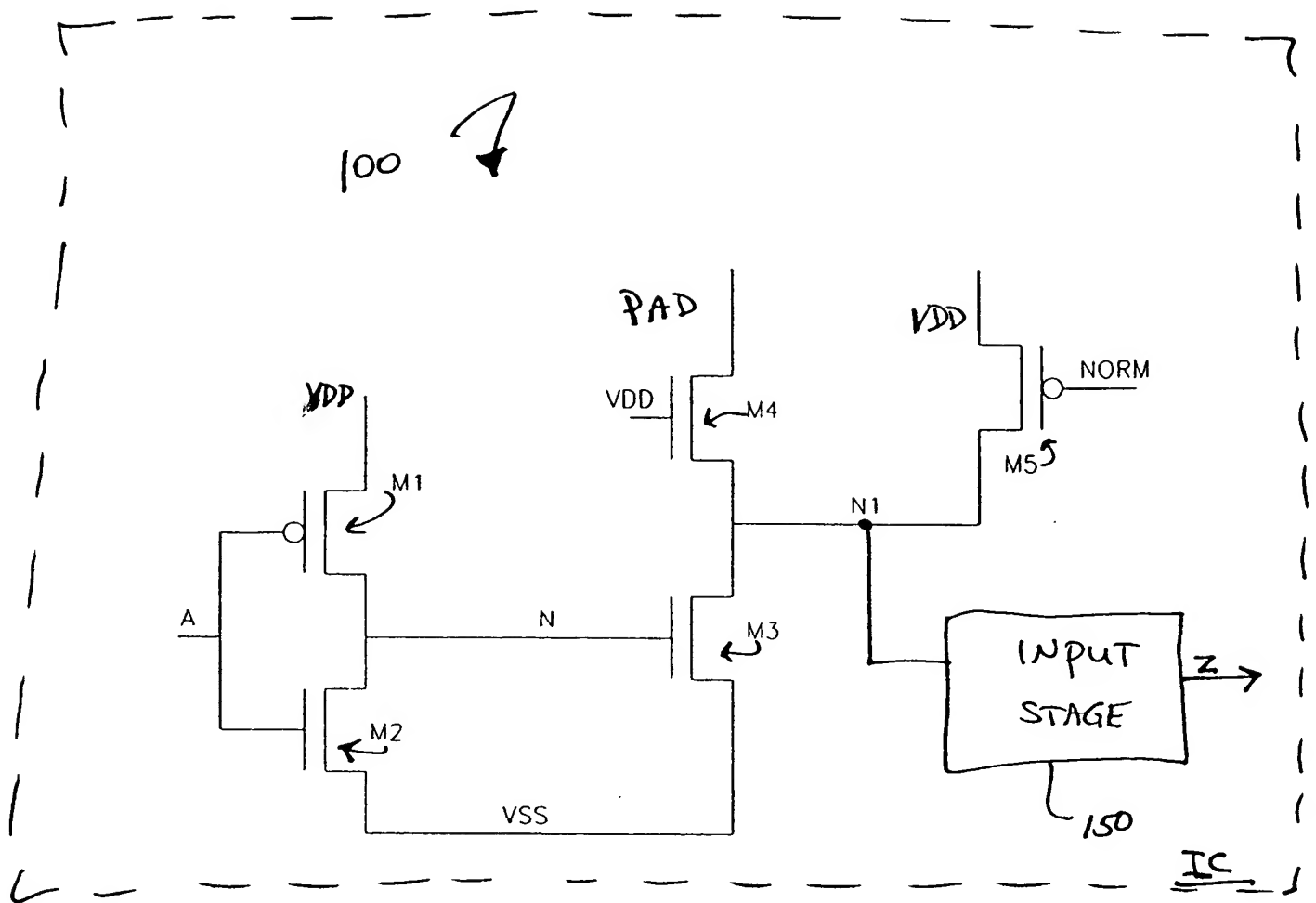


FIG. 3

Improved 5V Tolerant  
Open Drain Bi-Directional Buffer

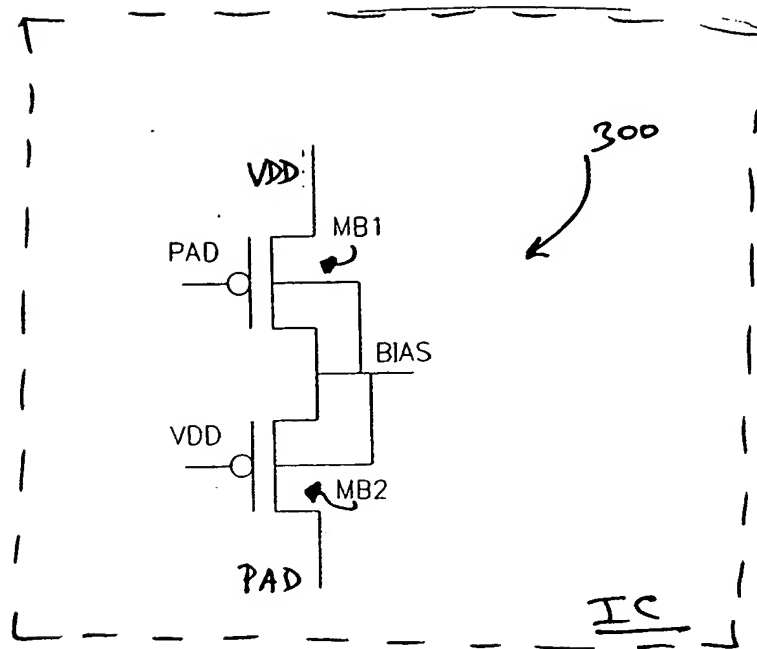
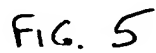


FIG. 4

Backgate Bias Generator



IC



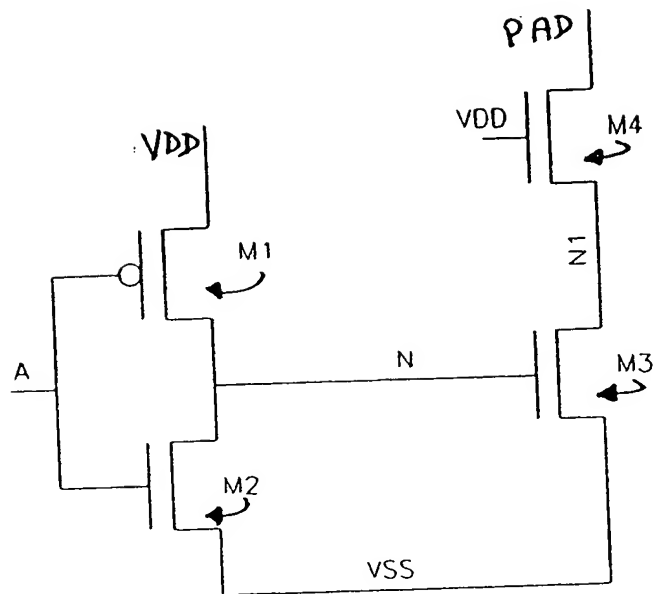


FIG. 7

5V Tolerant Open Drain Output Buffer  
(Prior Art)